

FIG. 1

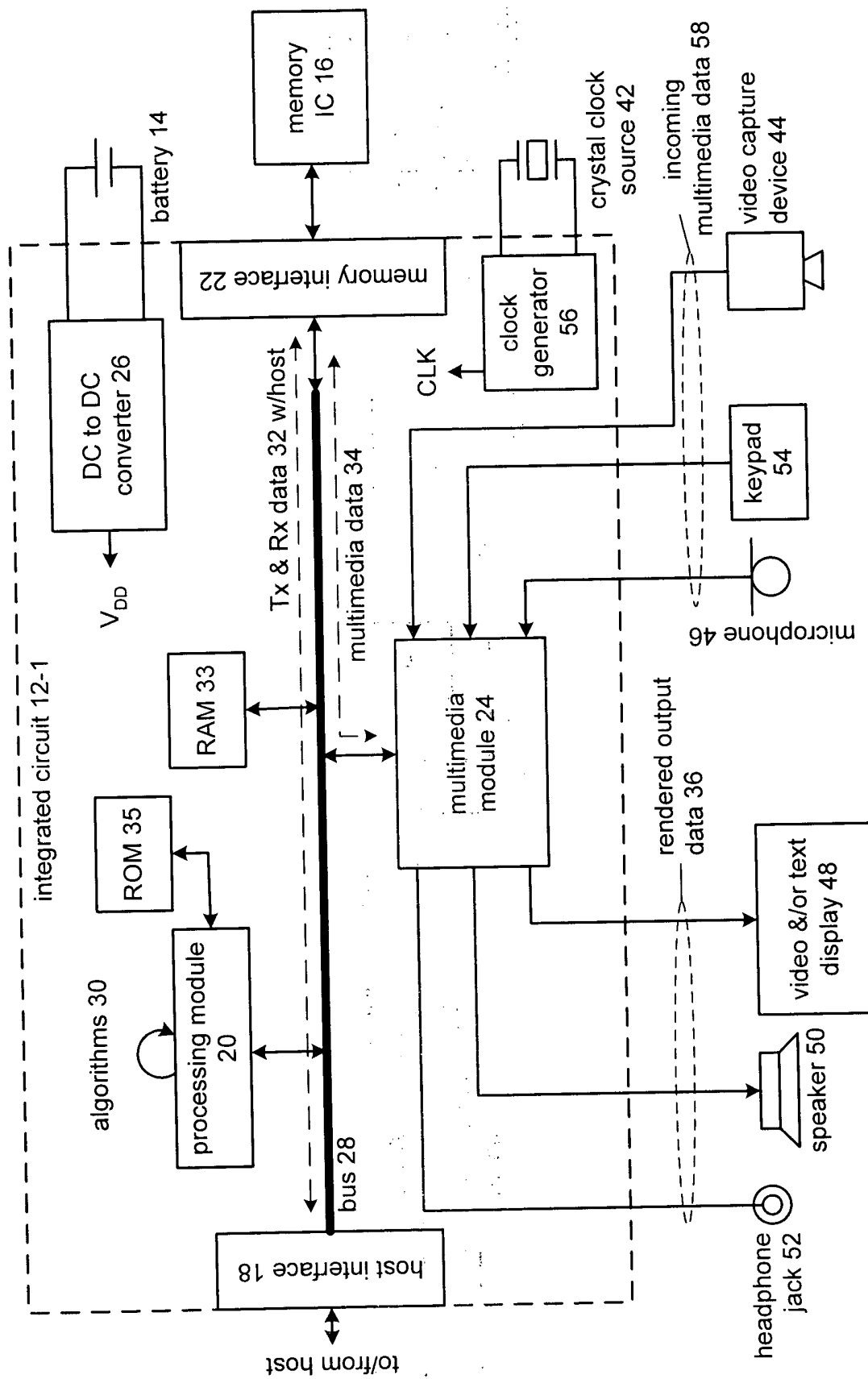


FIG. 2
multi-function handheld device 40

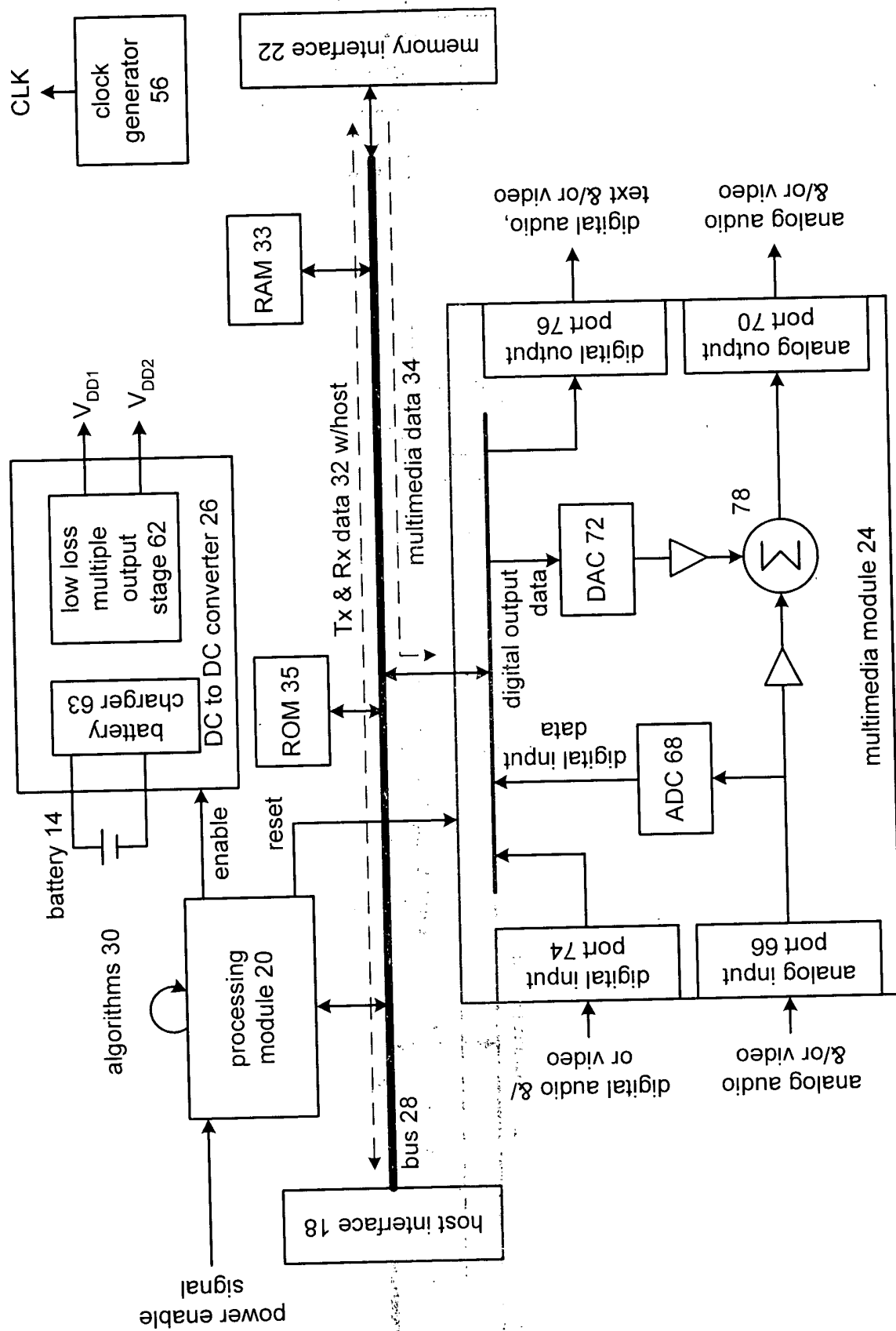


FIG. 3
integrated circuit 12-2

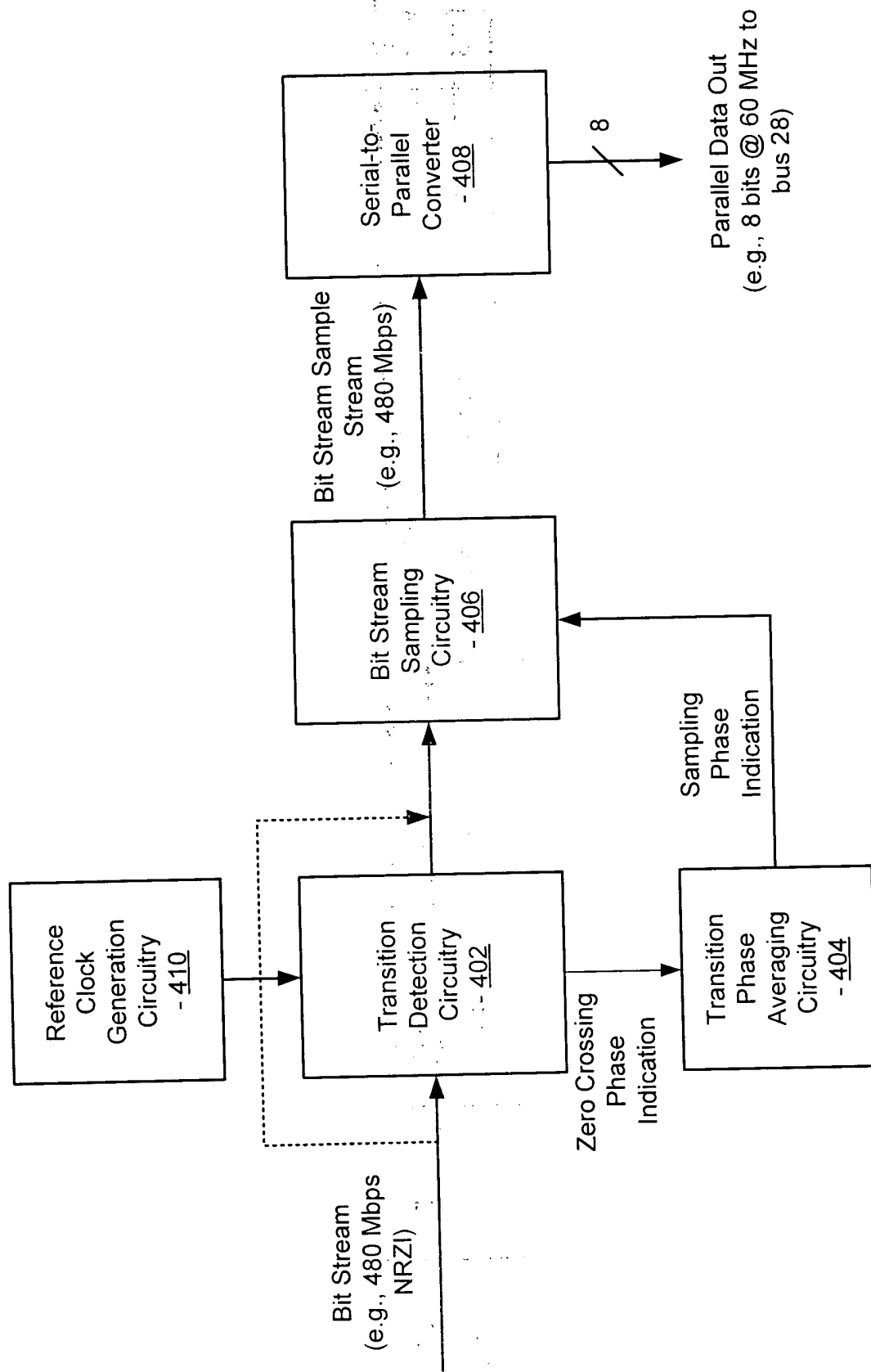


FIG. 4
Host Interface - 18

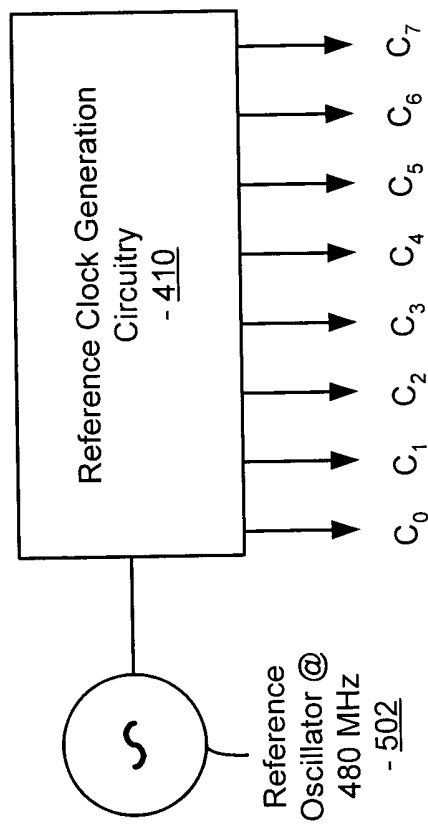


FIG. 5A

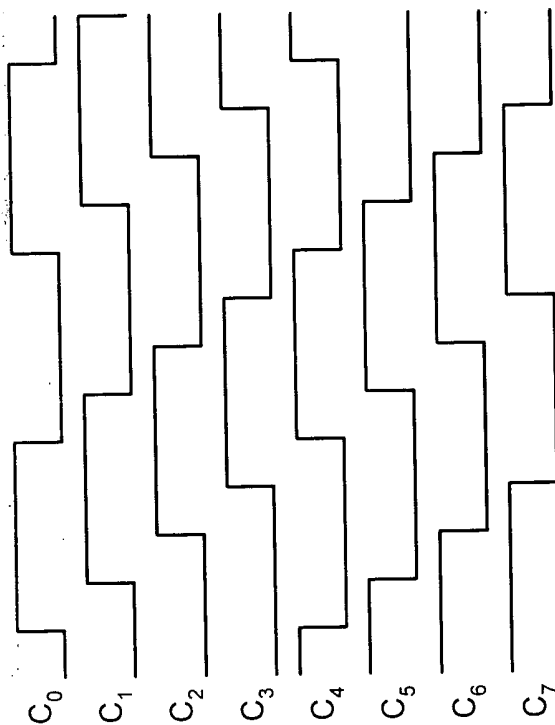


FIG. 5B

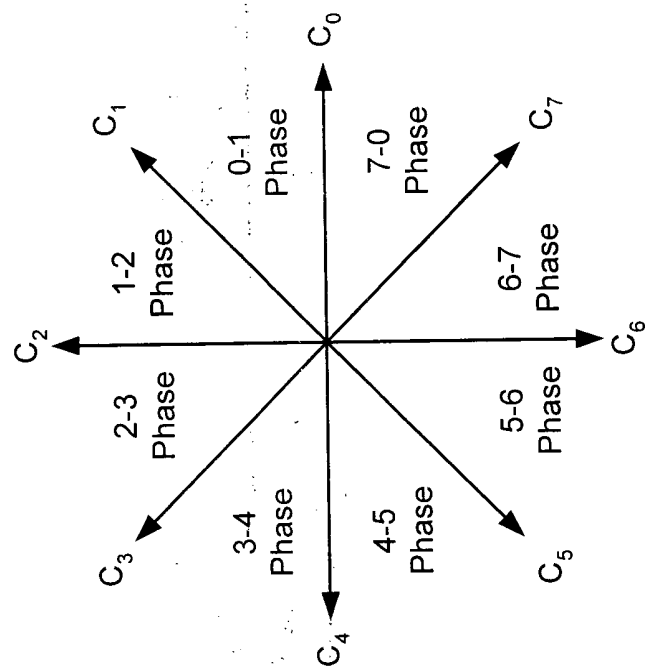
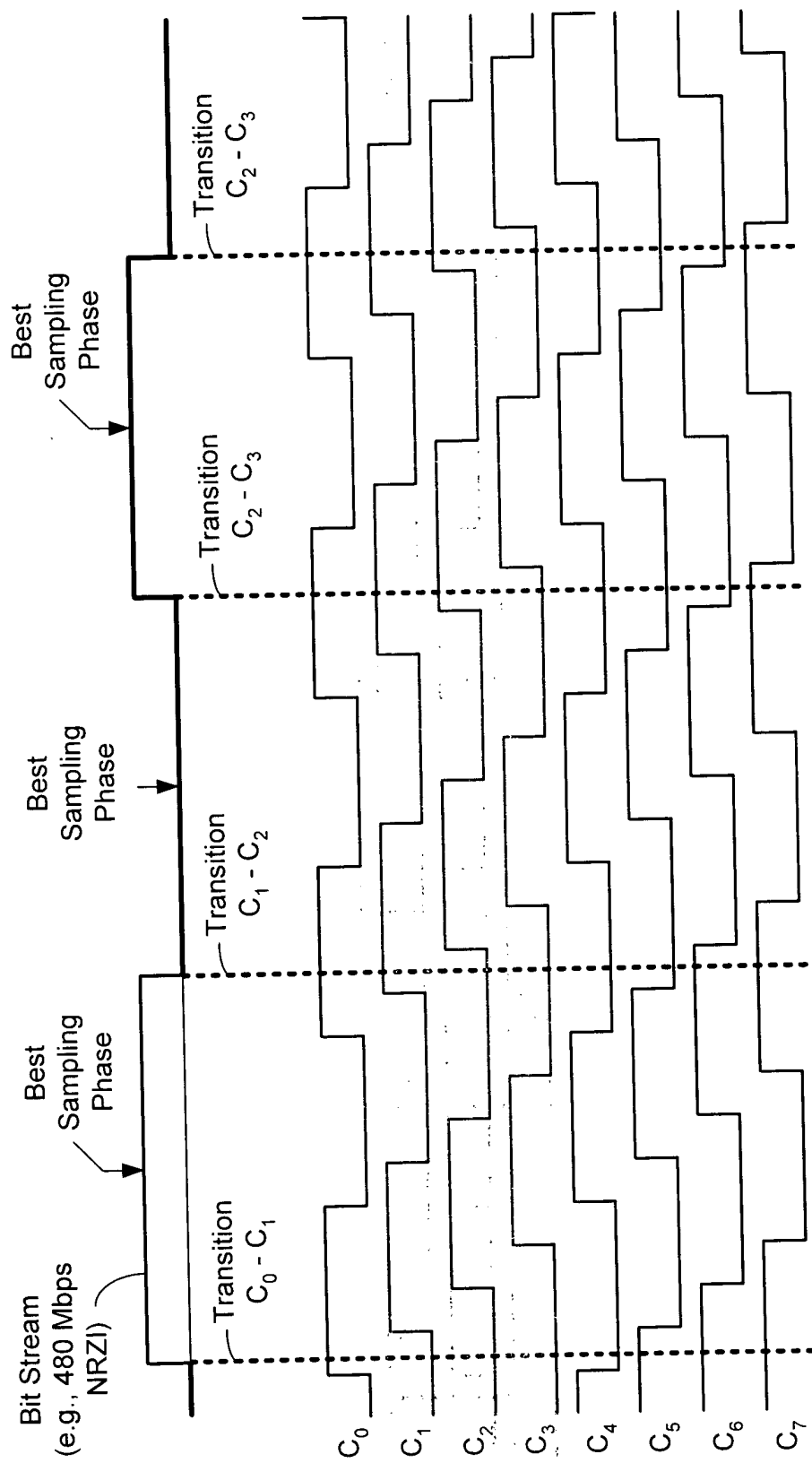


FIG. 5C



Eight Phase
480 MHz Clock

FIG. 6

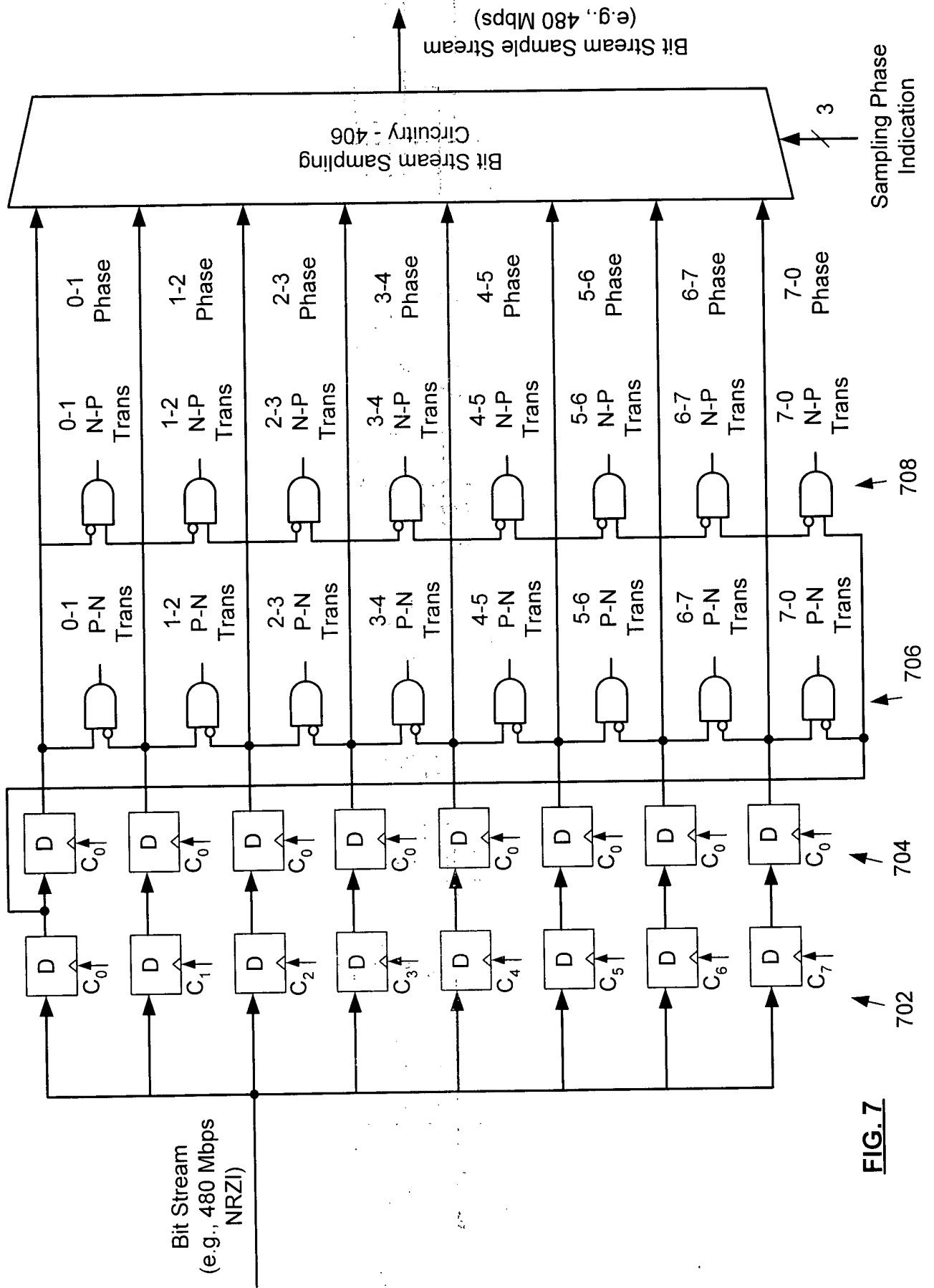


FIG. 7

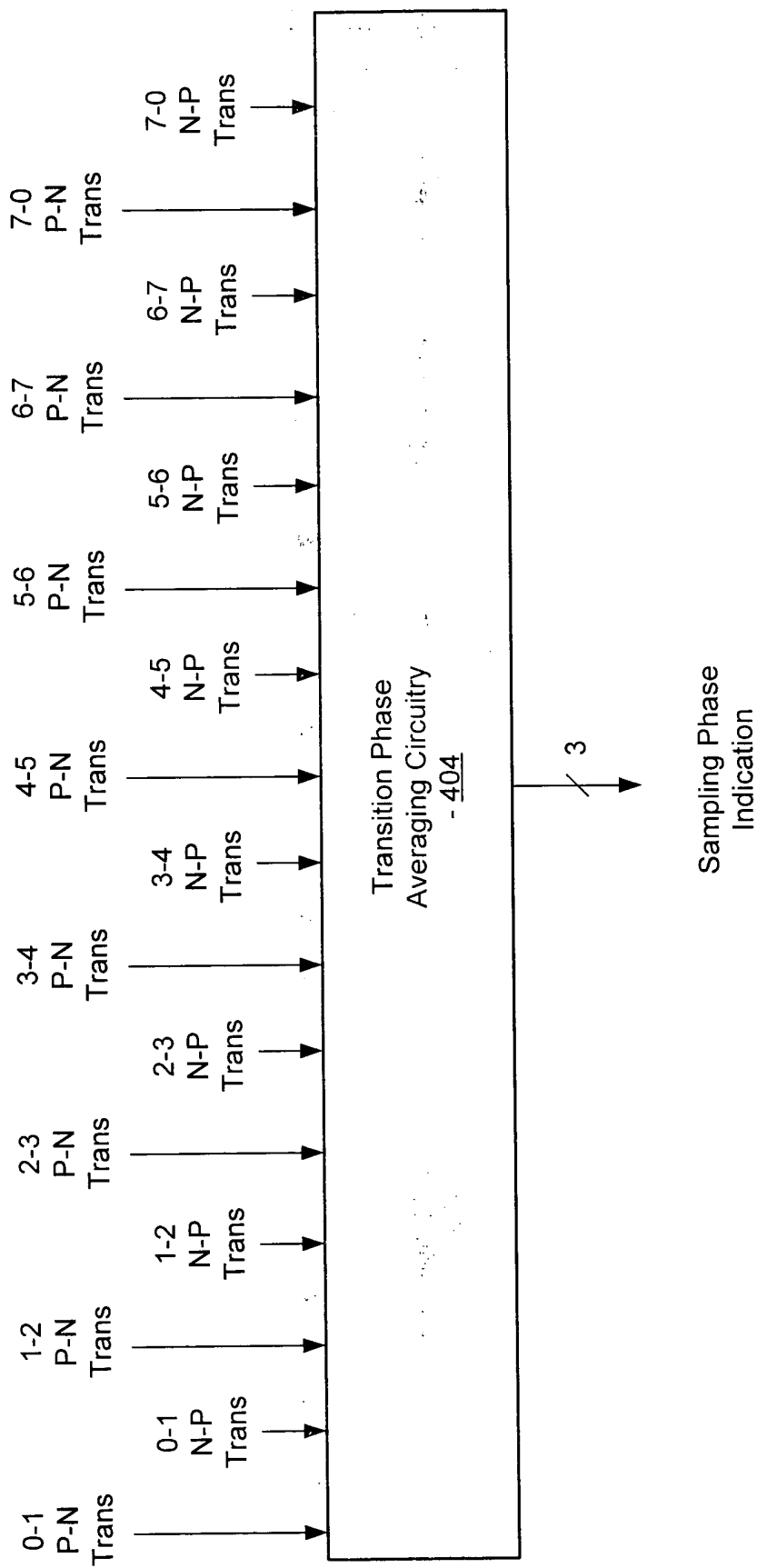


FIG. 8

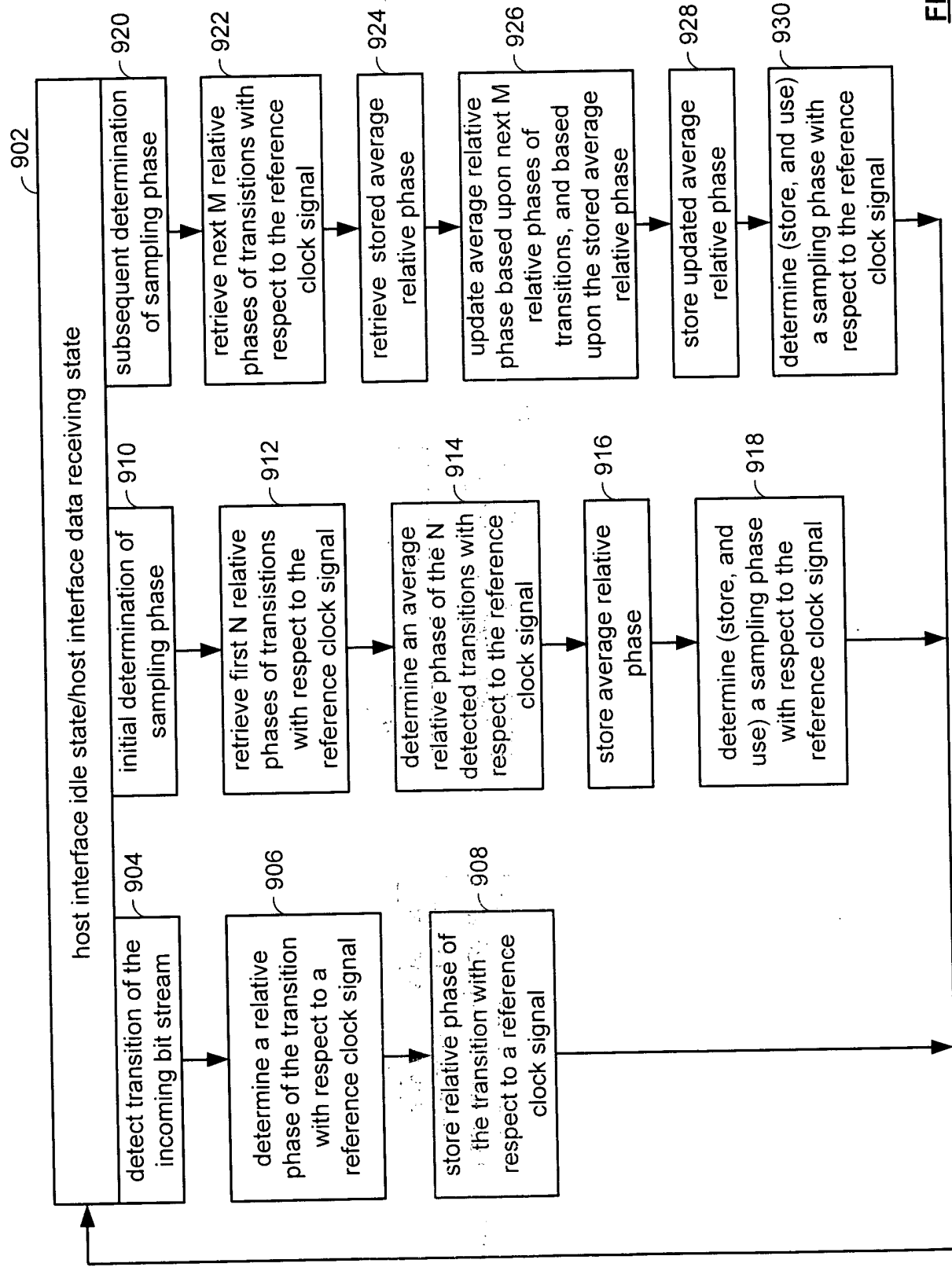


FIG. 9

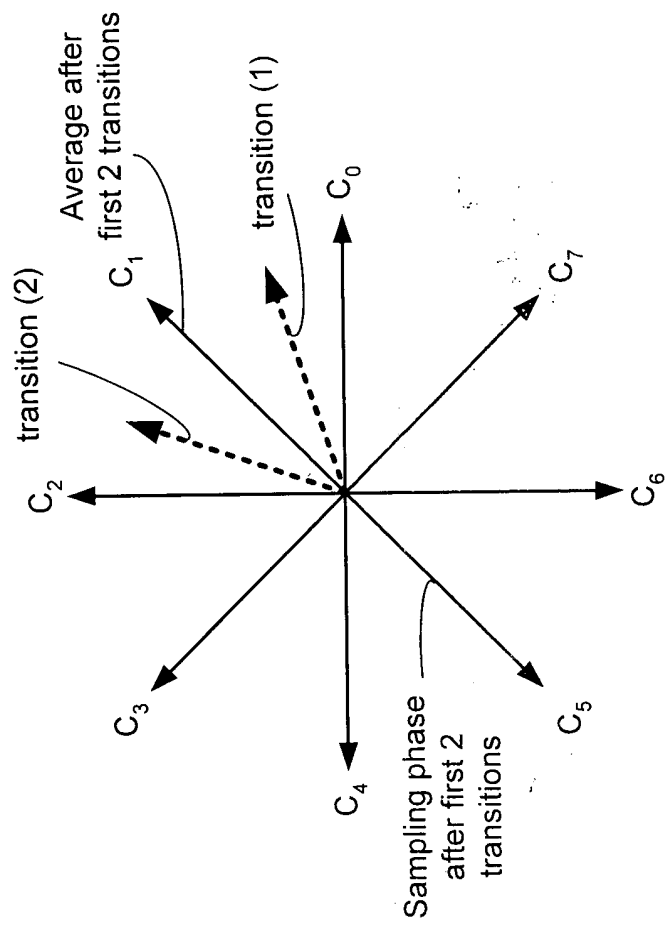


FIG. 10A

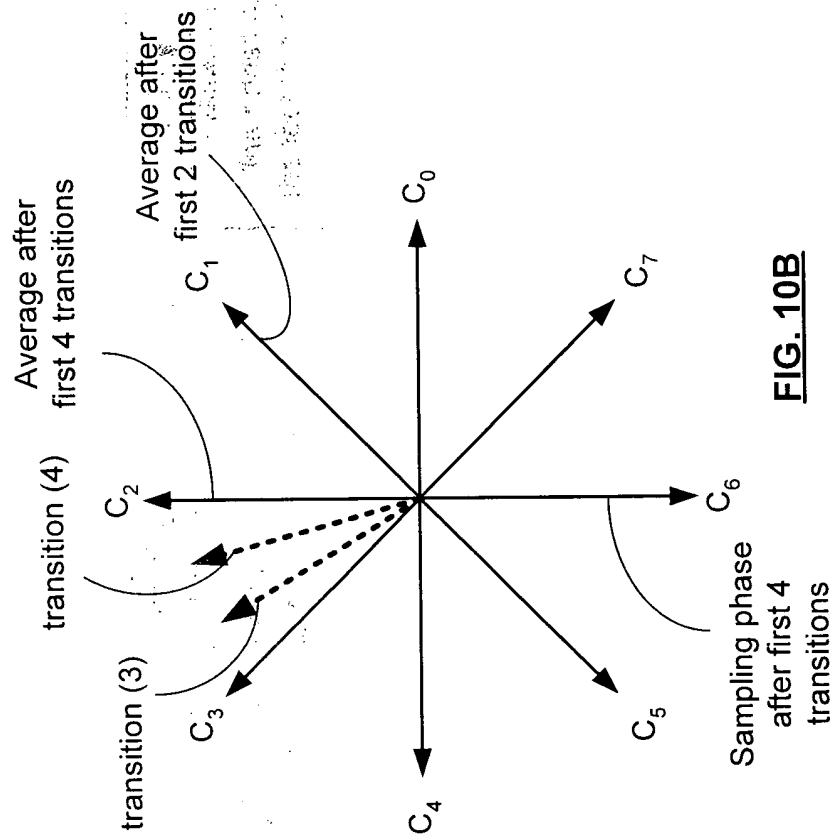


FIG. 10B

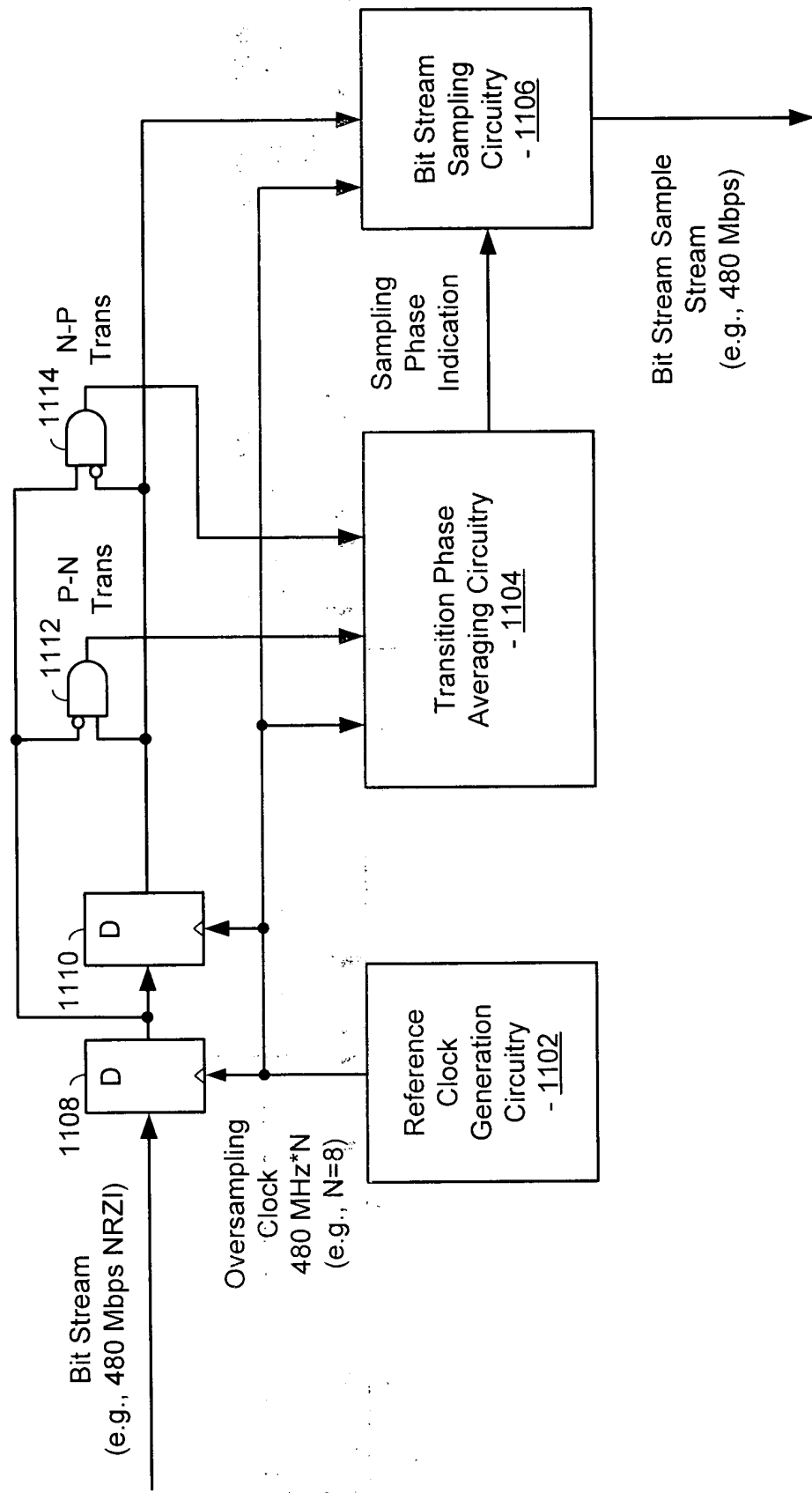


FIG. 11